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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Application Number	09/192,205
Filing Date	November 13, 1998
First Named Inventor	Gail Alverson
Group Art Unit	2756 TC 2700 MAIL ROOM
Examiner Name	

Attorney Docket No. 324758001US

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
MJ	AA	5,712,996		Schepers	1/27/98	
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MB	AC	5,557,761		Chan	9/17/96	
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		Office	Number				
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MB	AH	EP	EP0422945	International Business Machines	4/17/91		
MB	AI	EP	EP0864979	Digital Equipment Corporation	9/16/98		
MB	AJ	DE	DE19710252	Fujitsu Ltd.	2/26/98		
MB	AK	EP	EP0537098	International Business Machines	4/14/93		
MB	AL	EP	EP0855648	Texas Instruments, Inc.	7/29/98		
MB	AM	EP	EP0455966	International Business Machines	11/13/91		
MB	AN						

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MB	AO	Richard Korry et al., "Memory Management in the Tera MTA System," 1995.	
MB	AQ	Gail Alverson et al., "Processor Management in the Tera MTA System," 1995.	

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Mr. Ba

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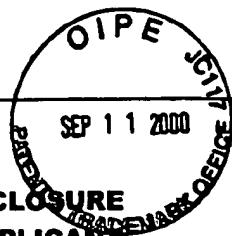
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2

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MB	AE	Touzeau, Roy F., "A Fortran Compiler for the FPS-164 Scientific Computer," Proceedings of the ACM SIGPLAN '84 Symposium on Compiler Construction, SIGPLAN Notices 19(6):48-57, June 1984.	
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MB	AL	Callahan, David, "Recognizing and Parallelizing Bounded Recurrences," August 1991.	

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Mark J. Anderson

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Sheet	3	of	4	Attorney Docket No.	324758001US

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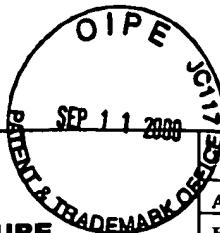
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MB	AH	H. Hayashi et al., "ALPHA: A High Performance Lisp Machine Equipped with a New Stack Structure and Garbage Collection System," 10 th Annual International Symposium on Computer Architecture, 1983.	
MB	AI	Smith, Burton, "Opportunities for Growth in High Performance Computing," November 1994.	
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